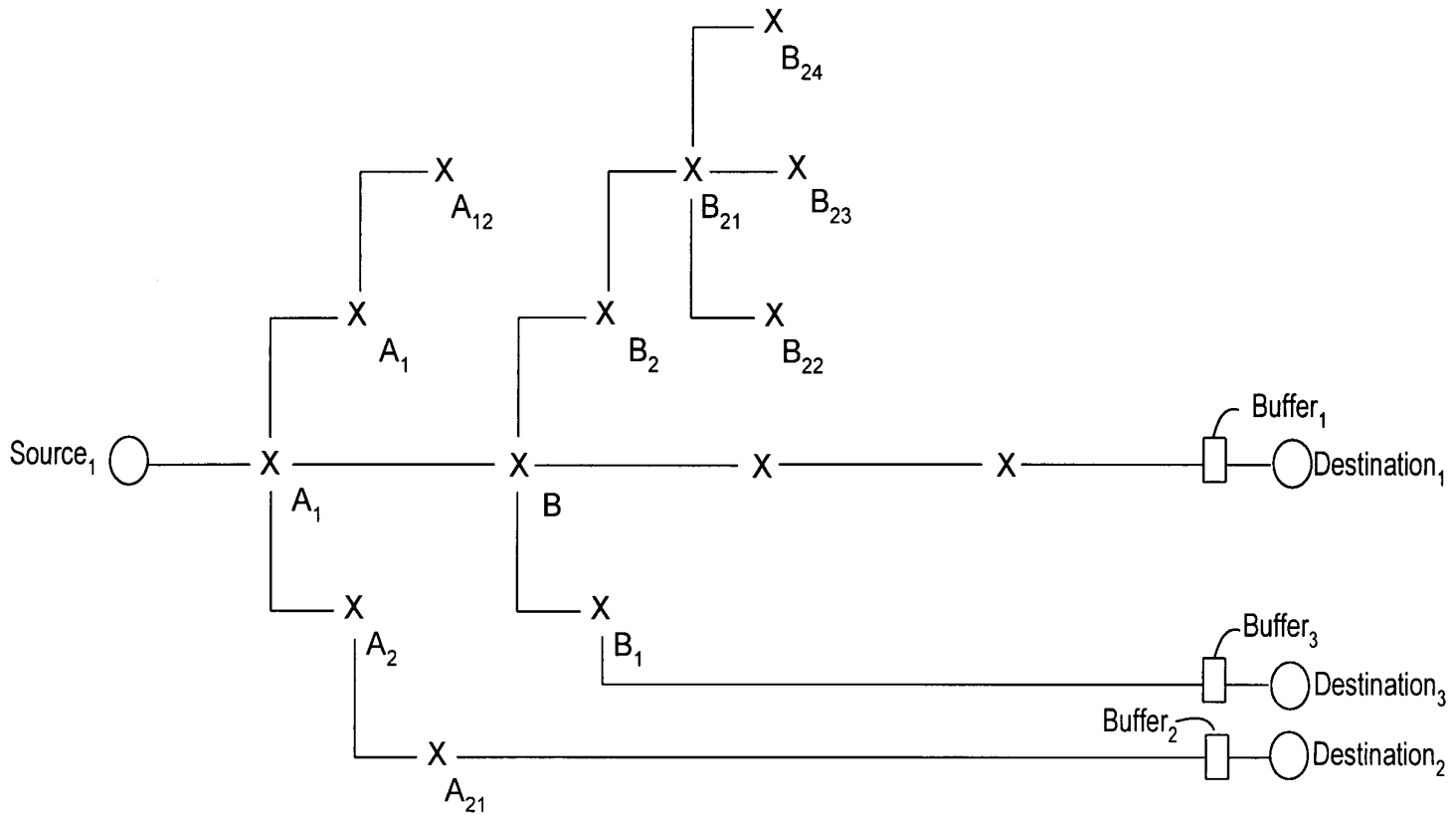

Figure 1
(Prior Art)

	A	B	C	D	E
Path 1	X	X		X	X
Path 2	X	X	X		X

Figure 2

	A	B	C	D	E	F
Path 1	X			X		X
Path 2	X	X	X		X	

Figure 3

	A	B	C	D	E	F
Path 1	X			X		X
Path 2	X	X	X		X	
Path 3		X				X

Figure 4

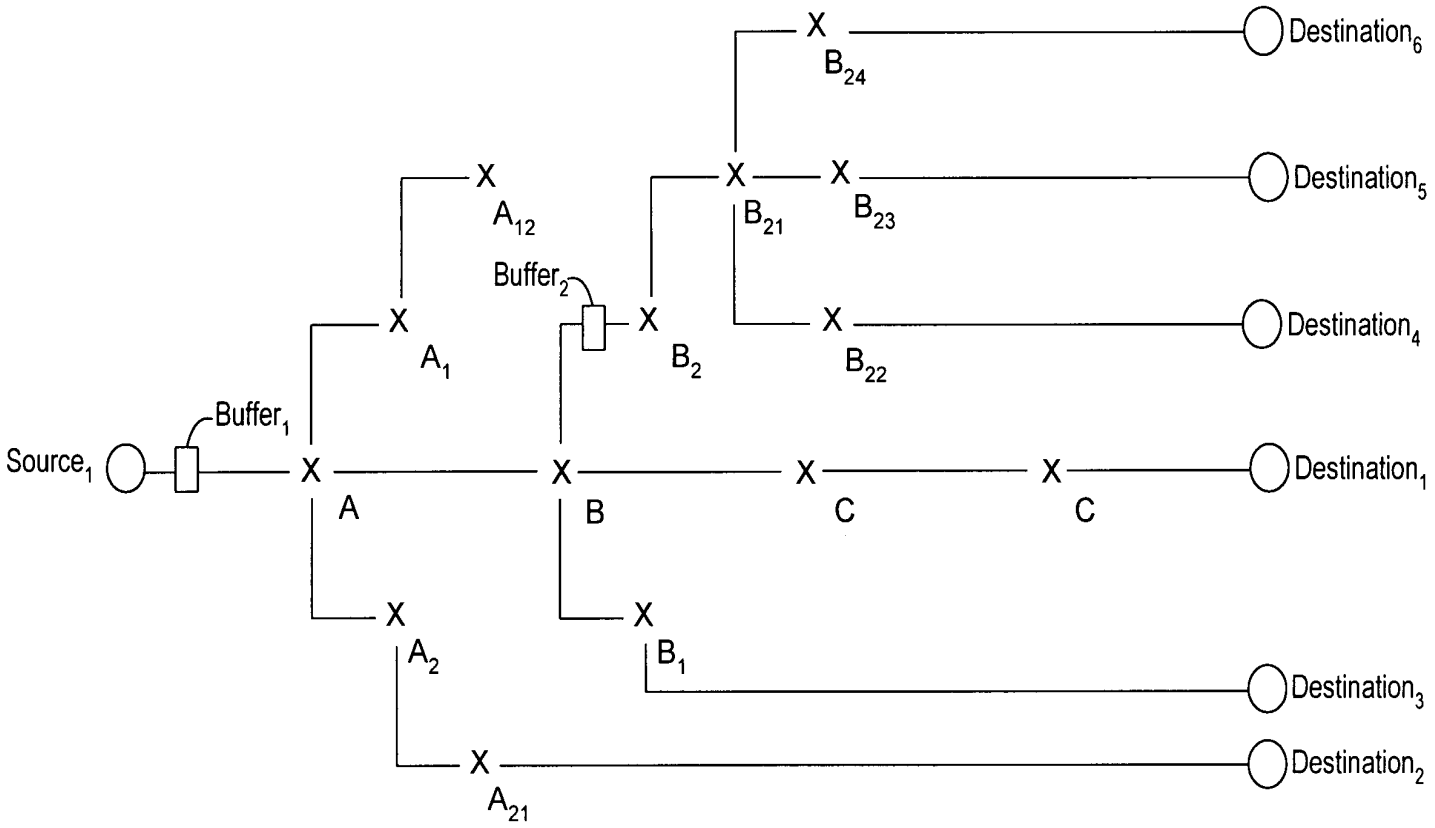
Source1
Buffer1
Buffer2
X
A
A1
A12
A2
A21
B
B1
B2
B21
B22
B23
B24
C
C
Destination1
Destination2
Destination3
Destination4
Destination5
Destination6

Figure 5

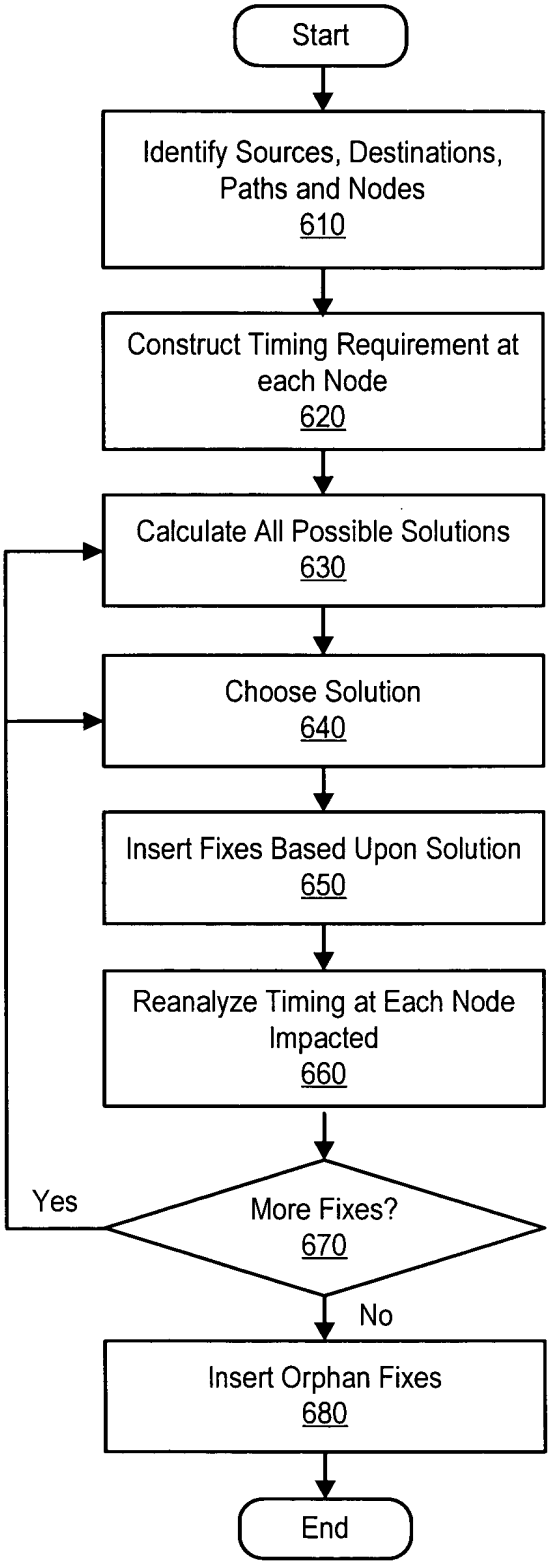
Start
Identify Sources, Destinations, Paths and Nodes
610
Construct Timing Requirement at each Node
620
Calculate All Possible Solutions
630
Choose Solution
640
Insert Fixes Based Upon Solution
650
Reanalyze Timing at Each Node Impacted
660
More Fixes?
670
Yes
No
Insert Orphan Fixes
680
End

Figure 6

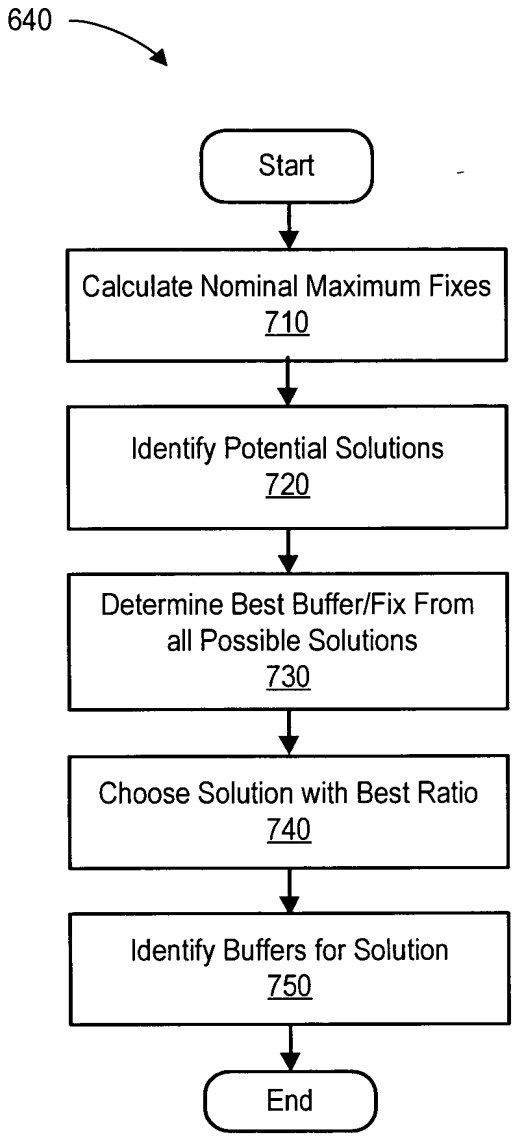
640
Start
Calculate Nominal Maximum Fixes
710
Identify Potential Solutions
720
Determine Best Buffer/Fix From all Possible Solutions
730
Choose Solution with Best Ratio
740
Identify Buffers for Solution
750
End

Figure 7

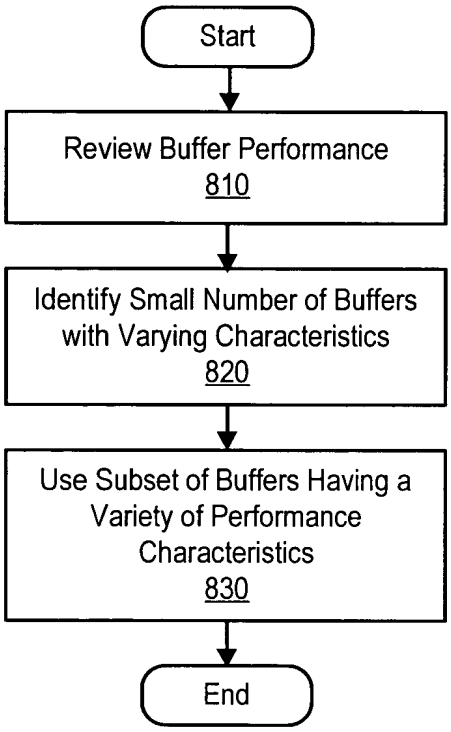
Start
Review Buffer Performance
810
Identify Small Number of Buffers with Varying Characteristics
820
Use Subset of Buffers Having a Variety of Performance Characteristics
830
End

Figure 8